

Schedule at a Glance

	May 28 Sun (Day 0)	May 29 Mon	May 30 Tue	May 31 Wed	Jun 1 Thu
Morning 1 (~1.5hr) 8:30am ~ 10:00am	Short course 1	Opening	Plenary 2	LVT-2 Vertical Low Voltage Devices (D1L-A) (3 papers)	SiC-3 Recent Advances in Ga2O3 Devices (D3L-A) (3 papers)
Morning break		Plenary 1			
Morning 2 (~2hr) 10:30am ~ 12:30pm		Coffee break	SiC-1 Novel Device Structures in SiC (A2L-A) (4 papers)	GaN-2 Novel GaN Power Devices and Technologies 1 (B2L-A) (4 papers)	SiC-2 Ruggedness & Reliability of SiC MOSFETs (C2L-A) (4 papers)
Lunch	Lunch	Lunch	Lunch	Lunch	Lunch
Afternoon 1 (~2hrs) 1:30pm ~ 3:30pm	Short course 2	GaN-1 GaN Power Device Reliability (A3L-A) (4 papers)	ICD ICs for Drivers and Protection Circuits (B3L-A) (5 papers)	PK Power Module Design and Development (C3L-A) (5 papers)	HV High Voltage Power Devices (C1L-A) (3 papers)
Afternoon break		Coffee break	Coffee break	Coffee break	Closing
Afternoon2 (~1.5hrs) 4:00pm ~ 5:30pm		LVT-1 Lateral Low Voltage Devices (A4L-A) (3 papers)	Poster-1 (SiC, LVT, PK) (30 papers)	Poster-2 (GaN, HV, ICD) (31 papers)	
Evening Activities		Reception	AdCom dinner/meeting (by invitation only)	Banquet	TPC dinner (by invitation only)

* The program is subject to change